

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. / 10559-607001 /	Application No. 10/054,179 /
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant William R. Wheeler et al. /	
(37 CFR §1.98(e))		Filing Date January 17, 2002 ✓	Group A/Unit 2123 4825

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0049957	04/2002	Hosono et al.			RECEIVED JUN 12 2003 Technology Center 2100
	AB	6,233,723	05/2001	Pribetich			
	AC	5,828,581	10/1998	Matumura			
	AD	2001/0018758	08/2001	Tanaka et al.			
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	AF	6,066,179	05/2000	Allan			
	AG	5,892,678	04/1999	Tokunoh et al.			
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	AI	5,513,119	04/1996	Moore et al.			
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Examiner Signature				Date Considered 29 Sept 2004			

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	AOO	6,053,947	04/2000	Parson			
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	AYY	6,401,230	06/2002	Ahanessians et al.			
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Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant William R. Wheeler et al.	TRADEMARK OFFICE JUN 1 2003 2123 2875
(37 CFR §1.98(b))		Filing Date January 17, 2002	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ACCC						

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes No
	ADDD						
	AEEE						
	AFFF						

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Examiner Initial	Desig. ID	Document					
	AGGG	Foley et al., "An Object Based Graphical User Interface for Power Systems", IEEE Transactions on Power Systems, Vol. 8, No. 1, February 1993, pp. 97-104.					
	AHHH	Pedram et al., "Floorplanning with Pin assignment", 1990 IEEE International Conference on Computer-Aided Design, 11 November 1990, pp. 98-101.					
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Substitute Disclosure Form (PTO-1449)

Substitute Form PTO-1449 (37 CFR 1.98(d))		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-607001	Application No. 10/054,179
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant William R. Wheeler et al.		RECEIVED MAR 17 2004	
		Filing Date January 17, 2002	Group Art Unit 2123 Technology Center 2100 28-25		

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January 17, 2002Group Art Unit
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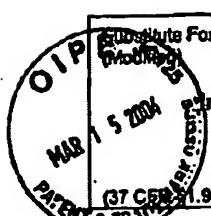
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	AHH	6,161,211	12/2000	Southgate			
	AII	6,178,541	01/2001	Joly, et al.			
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Patent and Trademark OfficeAttorney's Docket No.
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(37 CFR 1.98(b))

Applicant
William R. Wheeler et al.Filing Date
January 17, 2002Group Art Unit
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	AJJJ	6,591,407	07/2003	Kaufman, et al.			
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	ALLL	2002/0059054	05/2002	Bade, et al.			
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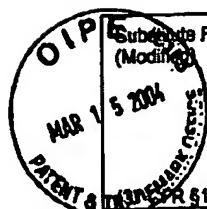
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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	ASSS	0 433 066	06/1991	EPO				
	ATTT	0 720 233	07/1996	EPO				
	AUUU	0 901 088	03/1999	EPO				
	AVVV	1 065 611	01/2001	EPO				
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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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	AHHHH	Lahti, et al., "SADE: a Graphical Toll for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
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	AJJJJ	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
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	AQQQQ	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", <i>IEEE International Conference on Systems Engineering</i> , August 9, 1990, pp. 474-477.

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